

CTP (I²C) PIN FUNCTION

Pin No.	Symbol	Description
1	VDD1	Power, VDD1=3.3V(type)
2	GND	Ground
3	RST	Reset, active high
4	SCL	I ² C Clock input
5	SDA	I ² C Data signal
6	INT	Interrupt output
7	NC	No Connect
8	NC	No Connect

REVISIONS

MARK	DATE	DESCRIPTION	DRAWN	CHECK	APPROVE

REVISIONS

MARK	DATE	DESCRIPTION	DRAWN	CHECK	APPROVE

UNLESS OTHERWISE NOTED
TOLERANCES : ±0.3

DATE: 14.10.31

PART NO. JYG-102460004G(R)-KT6L2-VB
DWG NO. G102460004G(R)-WX-VB
PAGE 1-1 UNIT mm

JIYA(LANGFANG) ELECTRONICS CO.,LTD

LCM Pins Description

Pin No.	Symbol	Description
1	VCOM	Common Voltage
2	VDD	Power Voltage for digital circuit
3	VDD	Power Voltage for digital circuit
4	NC	No connection
5	Reset	Global reset pin
6	STBYB	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source;driver will turn off, all output are High-Z
7	GND	Ground
8	RXIN0-	-LVDS differential data input
9	RXIN0+	+LVDS differential data input
10	GND	Ground
11	RXIN1-	-LVDS differential data input
12	RXIN1+	+LVDS differential data input
13	GND	Ground
14	RXIN2-	-LVDS differential data input
15	RXIN2+	+LVDS differential data input
16	GND	Ground
17	RXCCLKM-	-LVDS differential clock input
18	RXCCLKM+	+LVDS differential clock input
19	GND	Ground
20	RXIN3-	-LVDS differential data input
21	RXIN3+	+LVDS differential data input
22	GND	Ground
23	NC	No connection
24	NC	No connection
25	GND	Ground
26	NC	No connection
27	DIMO	Backlight GABC controller signal output

Pins Description

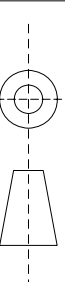
Pin No.	Symbol	Description
28	SELB	SELB="0",LVDS input data is 8 bits SELB="1",LVDS input data is 6 bits
29	AVDD	Power for Andlog Circuit
30	GND	Ground
31	LED-	LED Cathode
32	LED-	LED Cathode
33	L/R	Horizontal inversion,L/R="0", set right to left scan direction. When L/R="1", set left to right scan direction.
34	U/D	Vertical inversion,U/D="0", set top to bottom scan direction. When U/D="1", set bottom to top scan direction.
35	VGL	Gate OFF Voltage
36	CABGEN1	When CABG_EN="00", CABG OFF. When CABG_EN="01", user interface image. When CABG_EN="10", still picture. When CABG_EN="11", moving image.
37	CABGEN0	When CABG off, don't connect DIM0,else connect it to backlight.
38	VGH	Gate ON Voltage
39	LED+	LED Cathode
40	LED+	LED Cathode

NOTE:

- DISPLAY TYPE: TFT-NEGATIVE-TRANSMISSIVE
Normally White;Surface treatment:Glare
- VIEWING DIR.:6:00
- VDD=3.3±0.3V
- OP. TEMP.: -20°C~70°C
- ST. TEMP.: -30°C~70°C
- LVDS interface, & I2C CTP interface
- BACKLIGHT:LED COLDR-WHITE,If=180mA,Vf=9.3V<(TYP)<10.2V (MAX),X=0.26-0.36,Y=0.28-0.38V
Uniformity>70%, LUMINANCE>340CD/m² (420CD/m² (TYP) LCM)
- ROHS
- LCD DRAWING NO.: JY14A87
- TFT Module matching connector: Hirose FH12A-40S-0.5SH or equivalent
Sensor FPC matching connector: Molex 54548-0871 or equivalent

REVISIONS

MARK	DATE	DESCRIPTION	DRAWN	CHECK	APPROVE	NAME	DATE	UNLESS OTHERWISE NOTED TOLERANCES : ±0.3	DWG NO.	PAGE	UNIT
						LICHANGXING	14.10.31		G102460004(R)-WX-VB	1-2	mm



JIYA(LANGFANG) ELECTRONICS CO.,LTD

PRODUCT DRAWING

CUSTOMER DRAWING:

FILENAME

TOTAL PAGES: 3

PRODUCT NO.
JYG-102460004G(R)-KT6L2-VB

INITIATE REVISE

ORIGINAL DATE
14.10.31

REVISIONS

REV	DESCRIPTION	DATE