

REVISIONS						DRAWN	CHECK	APPROVE	PART NO.	JYC-640480066(r)-KT02-V0		
MARK	DATE	DESCRIPTION	DRAWN	CHECK	APPROVE							
						NAME			DWG NO.	G640480066(r)-WX-V0		
						DATE	15.4.23		SUB-DWG NO.	4		
						UNLESS OTHERWISE NOTED			PAGE	1-1	UNIT	mm
						TOLERANCES : F0.3			JY(A(LANGFANG) ELECTRONICS CO.,LTD			

CN1 Pin Connections

Pin NO.	SYMBOL	DESCRIPTION
1	U/D	Up or Down Display Control
2	NC	No Connection
3	Hsync	Horizontal SYNC.
4	VLED	Power Supply for LED Driver circuit
5	VLED	Power Supply for LED Driver circuit
6	VLED	Power Supply for LED Driver circuit
7	VCC	Power Supply for LCD
8	Vsync	Vertical SYNC.
9	DE	Data Enable
10	VSS	Power Ground
11	VSS	Power Ground
12	ADJ	Brightness control for LED B/L
13	B5	Blue Data 5 (MSB)
14	B4	Blue Data 4
15	B3	Blue Data 3
16	VSS	Power Ground
17	B2	Blue Data 2
18	B1	Blue Data 1
19	B0	Blue Data 0 (LSB)
20	VSS	Power Ground
21	G5	Green Data 5 (MSB)
22	G4	Green Data 4
23	G3	Green Data 3
24	VSS	Power Ground
25	G2	Green Data 2
26	G1	Green Data 1
27	G0	Green Data 0 (LSB)
28	VSS	Power Ground
29	R5	Red Data 5 (MSB)
30	R4	Red Data 4
31	R3	Red Data 3
32	VSS	Power Ground
33	R2	Red Data 2
34	R1	Red Data 1
35	R0	Red Data 0
36	VSS	Power Ground
37	VSS	Power Ground
38	DCLK	Clock Signals : Latch Data at the Falling Edge
39	VSS	Power Ground
40	L/R	Left or Right Display Control

L/R and U/D control Function

L/R	U/D	Function
1	0	Normally display
0	0	Left and Right opposite
1	1	Up and Down opposite
0	1	Left and Right opposite, Up and Down opposite

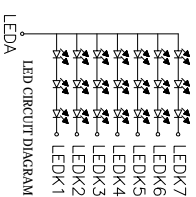
If DE signal is fixed low, SYNC mode is used. Otherwise, DE mode is used.

CTP Pin Connections

No.	Name	I/O	Description
1	NC	-	No connection
2	SCL	I	I2C Clock
3	SDA	I/O	I2C Data
4	NC	-	No connection
5	INT	O	Interrupt output pin to host
6	GND	P	Ground
7	VCC	P	Power; VCC=3.3V(typ.)
8	/RESET	I	CTP reset input pin, active low.
9	NC	-	No connection

NOTE:

1. DISPLAY TYPE: TFT digital-Super wide view
2. VIEWING DIR.:12:00
3. TFT VCC:3.3±0.3V
4. OP. TEMP.: -20°C~70°C
5. ST. TEMP.: -30°C~80°C
6. BACKLIGHT(LED)(10 dies) COLDR-WHITE, ILED=33mA(Typ.),400mA(MAX.),Vled=3.3V, VLED=5.0V, LUMINANCE:670CD/m<sup>2</sup>(Typ.) 530cd/m<sup>2</sup>(Min.)(with LCD)
7. ROHS
8. LCD DRAWING NO.:JY14B66
9. 40 PIN Connector:JAE FA5B040HP1 or Equivalent
10. 10 PIN Connector:Molex 54548-1071 or Equivalent
11. CTP FPC matching connector:Molex 52745-0997 or equivalent
12. CTP IC:IL12116



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						LTHUAFFEN	15.4.23				JYG-640480066(R)-KTCL2-V0	6640480066(R)-WX-V0	4	1-2	mm		
												UNLESS OTHERWISE NOTED TOLERANCES : ±0.3					
												JYA(LANGFANG) ELECTRONICS CO.,LTD					

# JIYAK(LANGFANG) ELECTRONICS CO.,LTD

PRODUCT DRAWING

CUSTOMER DRAWING:

FILENAME

TOTAL PAGES: 3

PRODUCT NO.  
JYG-64048006G(R)-KTCL2-VD

INITIATE

REVISE

ORIGINAL DATE  
15.4.23

## REVISIONS

REV

DESCRIPTION

DATE

REV	DESCRIPTION	DATE