

REVISIONS

MARK	DATE	DESCRIPTION	DRAWN	CHECK	APPROVE	NAME	DATE	UNLESS OTHERWISE NOTED TOLERANCES : ±0.3
						HuoXuepeng	2015.06.12	

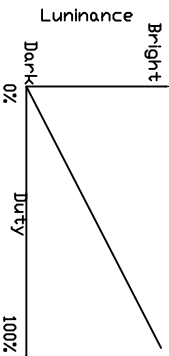
MARK	DATE	DESCRIPTION	DRAWN	CHECK	APPROVE	PART NO.	DWG NO.	SUB-DWG NO.	PAGE	UNIT	mm
						JYG-128080004G (R) -KT0L2-VA	G128080004G (R) -WX	4	1-1		

PIN DESCRIPTION		DESCRIPTION		DESCRIPTION	
PI SYMBOL		N	PI SYMBOL		
1	VCOM	Common Voltage	21	RXIN3+	-LVDS Differential Data Input(R6,R7,G6,G7,B6,B7)
2	VDD	Power Voltage for Digital Circuit	22	GND	Ground
3	VDD	Power Voltage for Digital Circuit	23	NC	No Connection
4	NC	No Connection	24	NC	No Connection
5	NC	No Connection	25	GND	Ground
6	NC	No Connection	26	NC	No Connection
7	GND	Ground	27	LED_PWM	Backlight CABG Controller Signal Output note2
8	RXIN0-	-LVDS Differential Data Input(R0~R5,G0)	28	NC	No Connection
9	RXIN0+	+LVDS Differential Data Input(R0~R5,G0)	29	AVDD	Power for Andlog Circuit
10	GND	Ground	30	GND	Ground
11	RXIN1-	-LVDS Differential Data Input(G1~G5,B0~B1)	31	LED-	LED Cathode
12	RXIN1+	+LVDS Differential Data Input(G1~G5,B0~B1)	32	LED-	LED Cathode
13	GND	Ground	33	NC	No Connection
14	RXIN2-	-LVDS Differential Data Input(B2~B5,HS,VS,DE)	34	NC	No Connection
15	RXIN2+	+LVDS Differential Data Input(B2~B5,HS,VS,DE)	35	VGL	Gate off Voltage
16	GND	Ground	36	NC	No Connection
17	RXCJK-	-LVDS Differential Clock Input(LVDS Clock)	37	CABG_EN	CABG Enable Input
18	RXCJK+	+LVDS Differential Clock Input(LVDS Clock)	38	VGH	Gate on Voltage
19	GND	Ground	39	LED+	LED Anode
20	RXIN3-	-LVDS Differential Data Input(R6,R7,G6,G7,B6,B7)	40	LED+	LED Anode

NOTE1: The setting of CABG function as follows

Pin	Enable	Disenable
CABG_EN	High Voltage	Low Voltage or Open

NOTE2: LED_PWM is used to adjust backlight brightness



CTP PIN DESCRIPTION	DESCRIPTION
1	VDD1 Power VDD1=3.3V (TYP)
2	GND Ground
3	GND Ground
4	SCL I ² C Clock Input
5	SDA I ² C Data Input
6	GND Ground
7	INT Interrupt Output Pin
8	RESET CTP Reset Input Pin

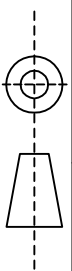
IC: Pixcir C44+F32 Datasheet

NOTE:

- 1.DISPLAY TYPE:FT NEGATIVE, TRANSMISSIVE
- 2.VIEW DIRECTION: ALL.
- 3.DRIVE METHOD:VDD = 2.50±0.2V.
- 4.OP.TEMP. :-20°C~+70°C
- 5.ST.TEMP. :-30°C~+80°C
- 6.INTERFACE TYPE:LVDS.
- 7.BACKLIGHT:LED, White Color.
- If=240ma; Vf:16.8V(Min),19.5V(TYP),21V(Max);
- LCM LUMINANCE of White:340CD/m²(MIN),420CD/m²(TYP).
- 8.RHOS.
- 9.LCD DRAWING NO.: JY15A96.
- 10.CTP Matching connector :Molex 52204-0860 or equivalent
- 11.LCM connector :Vigorconn F62240-h12100 or equivalent

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JIYA(LANGFANG) ELECTRONICS CO.,LTD

PRODUCT DRAWING		CUSTOMER DRAWING	
		FILENAME	TOTAL PAGES 3
PRODUCT NO. JYG-102480004G(R)-KT0L2-VA		<input checked="" type="checkbox"/> INITIATE <input type="checkbox"/> REVISE	ORIGINAL DATE 2015.06.12
REVISIONS			
REV	DESCRIPTION	DATE	

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